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"RESPONSE UNDER 37 CFR 1.116-
EXEMPTED PROCEDURE EXAMINING
GROUP 2826"

Docket No.: 216932US2

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COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/003,114

Applicants: Masaki YAMADA, et al.

Filing Date: December 6, 2001

For: SEMICONDUCTOR DEVICE HAVING BURIED
CONDUCTIVE LAYER FORMED IN AN
INSULATING LAYER AND METHOD OF
MANUFACTURING THEREOF

Group Art Unit: 2826

Examiner: Examiner Greene, P.L.

SIR:

Attached hereto for filing are the following papers:

RESPONSE (AFTER FINAL)

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2826"

DOCKET NO: 216932US2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

MASAKI YAMADA, ET AL.

: EXAMINER: GREENE, P.

SERIAL NO: 10/003,114

:

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: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR DEVICE
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THEREOF

RESPONSE

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated May 20, 2004, please amend the above-identified application as follows:

Remarks/Arguments begin on page 2 of this paper.